



100V

45A

<17 mohm

N-Channel Enhancement Mode Field Effect Transistor



Product Summary

| • | VDS |
|---|--|
| • | lo |
| • | R _{DS(ON)} (at V _{GS} =10 |

- Rps OV)
- 100% UIS Tested
- 100% ∇V_{DS} Tested

General Description

- Low R_{DS(on)} & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Fast switching and soft recovery

Applications

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

■ Absolute Maximum Ratings (T_A=25[°]C unless otherwise noted)

| Parameter | | Symbol | Limit | Unit | |
|--|----------------------|----------------------------------|----------|------|--|
| Drain-source Voltage | | V _{DS} | 100 | V | |
| Gate-source Voltage | | V _{GS} | ±20 | V | |
| Drain Current | T _c =25℃ | | 45 | • | |
| Drain Current | T _c =100℃ | I _D | 28.5 | A | |
| Pulsed Drain Current ^A | | I _{DM} | 180 | А | |
| Avalanche energy ^B | | Eas | 81 | mJ | |
| Total Power Dissipation ^c | Tc=25℃ | P₀ | 72 | W | |
| | Tc=100℃ | ΓD | 28.8 | vv | |
| Junction and Storage Temperature Range | | T _J ,T _{STG} | -55~+150 | °C | |

■Thermal resistance

TO-252

| Parameter | Symbol | Тур | Max | Units | |
|---|--------------|------------------|------|-------|------|
| Thermal Resistance Junction-to-Ambient ^D | t≪10S | P | 15 | 20 | |
| Thermal Resistance Junction-to-Ambient ^D | Steady-State | R _{0JA} | 40 | 50 | °C/W |
| Thermal Resistance Junction-to-Case | Steady-State | R _{eJC} | 1.35 | 1.7 | |

Ordering Information (Example)

| PREFERED P/N | PACKING CODE | Marking | MINIMUM PACKAGE(pcs) | INNER BOX QUANTITY(pcs) | OUTER CARTON QUANTITY(pcs) | DELIVERY MODE |
|--------------|-----------------|------------|-------------------------|----------------------------|-------------------------------|---------------|
| HNS45G10HA | F1 | HNS45G10HA | 2500 | / | 25000 | 13"Reel |



HNS45G10HA

■ Electrical Characteristics (Tj=25°C unless otherwise noted)

| Parameter | Symbol | Conditions | Min | Тур | Max | Units |
|---------------------------------------|---------------------|--|-----|------|------|-------|
| Static Parameter | l | | | • | | L |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} = 0V, I _D =250µA | 100 | | | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =100,V _{GS} =0V | | | 1 | μA |
| Gate-Body Leakage Current | I _{GSS} | V_{GS} = ±20V, V_{DS} =0V | | | ±100 | nA |
| Gate Threshold Voltage | V _{GS(th)} | V_{DS} = V_{GS} , I_D =250 μ A | 2 | 2.8 | 4 | V |
| Static Drain-Source On-Resistance | R _{DS(ON)} | V _{GS} = 10V, I _D =20A | | 14 | 17 | mΩ |
| Diode Forward Voltage | V _{SD} | I _S =20A,V _{GS} =0V | | | 1.3 | V |
| Maximum Body-Diode Continuous Current | Is | | | | 45 | А |
| Gate resistance | R _G | f= 1 MHz, Open drain | | 1 | | Ω |
| Dynamic Parameters | · | | | | | |
| Input Capacitance | C _{iss} | | | 1135 | | |
| Output Capacitance | Coss | V _{DS} =50V,V _{GS} =0V,f=1MHZ | | 399 | | pF |
| Reverse Transfer Capacitance | C _{rss} | | | 18 | | |
| Switching Parameters | | | | | | |
| Total Gate Charge | Qg | | | 16 | | |
| Gate-Source Charge | Q _{gs} | V _{GS} =10V,V _{DS} =50V,I _D =25A | | 5.6 | | |
| Gate-Drain Charge | Q _{gd} | | | 2.4 | | nC |
| Reverse Recovery Chrage | Q _{rr} | | | 42 | | |
| Reverse Recovery Time | t _{rr} | I _F =20A, di/dt=100A/us | | 39.8 | | |
| Turn-on Delay Time | t _{D(on)} | | | 39.2 | | |
| Turn-on Rise Time | tr | V _{GS} =10V, V _{DD} =50V,I _D =25A | | 11 | | ns |
| Turn-off Delay Time | t _{D(off)} | $R_{GEN}=2.2\Omega$ | | 53.2 | | |
| Turn-off fall Time | t _f | | | 15.8 | | |

A. Repetitive rating; pulse width limited by max. junction temperature.

B. V_{DD}=50V, R_G=25Ω, L=0.5mH, I_{AS}=25A,.

C. Pd is based on max. junction temperature, using junction-case thermal resistance.

D. The value of RqJA is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C. The Power dissipation PDSM is based on R qJA t≤ 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.



HNS45G10HA

Typical Performance Characteristics



Figure 1. Output Characteristics

1000

100

10

1

0.1

Capacitance (pF)









1





Figure4. Gate Charge

16



Figure6.Normalized On-Resistance







Figure7. Drain current

Figure8.Safe Operation Area



Figure9.Normalized Maximum Transient thermal impedance



■ TO-252 Package information





Α2





| | DIMENSIONS | | | | | |
|----------------|------------|----------|-------|------------------|----------------|--------|
| OVMDEN | | INCHES | | 1 | Millimeter | ^ |
| SYMBOL | MIN. | NDM. | MAX. | MIN. | NDM. | MAX. |
| A1 | 0,000 | | 0.008 | 0.000 | | 0.200 |
| A2 | 0,087 | 0.091 | 0.094 | 2,200 | 2,300 | 2,400 |
| A 3 | 0.035 | 0,039 | 0.043 | 0.900 | 1.000 | 1.100 |
| b | 0.026 | 0,030 | 0,034 | 0,660 | 0.760 | 0.860 |
| С | 0.018 | 0,020 | 0.023 | 0,460 | 0.520 | 0,580 |
| D | 0.256 | 0,260 | 0,264 | 6.500 | 6,600 | 6.700 |
| D1 | 0.203 | 0,209 | 0.215 | 5.150 | 5,300 | 5,450 |
| D2 | 0.181 | 0.189 | 0,195 | 4.600 | 4,800 | 4,950 |
| Е | 0.390 | 0,398 | 0,406 | 9,900 | 10.100 | 10.300 |
| E1 | 0,236 | 0,240 | 0,244 | 6,000 | 6 <u>.1</u> 00 | 6,200 |
| E2 | 0,203 | 0,209 | 0.215 | 5,150 | 5,300 | 5,450 |
| e | | 0,090BSC | | | 2.286BS | С |
| L | 0.049 | 0.059 | 0.069 | 1,250 | 1,500 | 1.750 |
| L1 | 0,035 | | 0,050 | 0.900 | | 1.270 |
| L2 | 0,055 | | 0,075 | 1,400 | | 1,900 |
| L3 | 0,240 | 0.310 | 0.039 | 0.600 0.800 1.00 | | |
| L4 | 0.114REF | | | 2.900REF | | |
| θ | 0* | | 10* | 0* 10* | | |

BOTTOM VIEW

Ш

SUGGESTED SOLDER PAD LAYOUT

Attention

1,Any and all Huaban products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and /or material damage. Consult with your Huaban representative nearest you before using any Huaban products described or contained herein in such applications.

2,Huaban assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Huaban products described or contained herein.

3, Specifications of any and all Huaban products described or contained here instipulate the performance, characteristics, functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

4, Huaban Semiconductor CO., LTD. strives to supply high quality high reliabilityproducts. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures

could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. Whendesigning equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

5, In the event that any or all Huaban products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products

must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HuabanSemiconductor CO., LTD.

7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Huaban believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "DeliverySpecification" for the Huaban product that you Intend to use.